

	Search Text
1	(bidirection\$4 bi adj direction\$4) same (unidirection\$4 uni adj direction\$4) and emulat\$4 and simulat\$4
2	(bidirection\$4 bi adj direction\$4) same (unidirection\$4 uni adj direction\$4) and emulat\$4 and simulat\$4 and bus near3 (interfac\$4 protocol communication)
3	(bidirection\$4 bi adj direction\$4) same (unidirection\$4 uni adj direction\$4) and emulat\$4 and simulat\$4 and bus near3 (interfac\$4 protocol communication) and (tristate adj (buffer driver) or (tri adj state) adj (buffer driver))
4	(bidirection\$4 bi adj direction\$4) same (unidirection\$4 uni adj direction\$4) and emulat\$4 and simulat\$4 and bus near3 (interfac\$4 protocol communication) and (tristate adj (buffer driver) or (tri adj state) adj (buffer driver)) and emulat\$4 with (bus\$4 signal channel)
5	link and channel and (bidirection\$4 bi adj direction\$4) and (unidirection\$4 uni adj direction\$4)
6	link same emulat\$4 and channel and (bidirection\$4 bi adj direction\$4) and (unidirection\$4 uni adj direction\$4)
7	link same emulat\$4 and channel and (bidirection\$4 bi adj direction\$4) and (unidirection\$4 uni adj direction\$4) and bus near3 (interfac\$4 protocol communication arbitration )
8	determin\$4 same signal same direction same emulation
9	(conver\$4) same (bidirection\$4 bi adj direction\$4) same (unidirection\$4 uni adj direction\$4)
10	(conver\$4) same (bidirection\$4 bi adj direction\$4) same (unidirection\$4 uni adj direction\$4) and emulat\$4 same (bidirection\$4 bi adj direction\$4 unidirection\$4 uni adj direction\$4 direction\$4 )
11	determin\$4 same signal same direction same emulation and (bidirection\$4 bi adj direction\$4) same (unidirection\$4 uni adj direction\$4)
12	channel same direction same emulation and (bidirection\$4 bi adj direction\$4) same (unidirection\$4 uni adj direction\$4)
13	(bidirection\$4 bi adj direction\$4) and (unidirection\$4 uni adj direction\$4) and bus near3 (arbitration interfac\$4 protocol communication)
14	(bidirection\$4 bi adj direction\$4) same emulat\$4 and (unidirection\$4 uni adj direction\$4) same emulat\$4 and bus near3 (arbitration interfac\$4 protocol communication) and (tristate adj (buffer driver) or (tri adj state) adj (buffer driver))
15	(bidirection\$4 bi adj direction\$4) and (unidirection\$4 uni adj direction\$4) and bus near3 (arbitration interfac\$4 protocol communication) and (tristate adj (buffer driver) or (tri adj state) adj (buffer driver))
16	emulat\$4 with direction\$4 (bidirection\$4 bi adj direction\$4) and (unidirection\$4 uni adj direction\$4) and bus near3 (arbitration interfac\$4 protocol communication) and (tristate adj (buffer driver) or (tri adj state) adj (buffer driver))
17	emulat\$4 with direction\$4 and (bidirection\$4 bi adj direction\$4) and (unidirection\$4 uni adj direction\$4) and bus near3 (arbitration interfac\$4 protocol communication) and (tristate adj (buffer driver) or (tri adj state) adj (buffer driver))
18	emulat\$4 with direction\$4 and (bidirection\$4 bi adj direction\$4) and (unidirection\$4 uni adj direction\$4) "."
19	emulat\$4 with direction\$4 and simulat\$4 with direction\$4 and (bidirection\$4 bi adj direction\$4) and (unidirection\$4 uni adj direction\$4) "."
20	link same emulat\$4 and channel and (bidirection\$4 bi adj direction\$4) and (unidirection\$4 uni adj direction\$4) and bus near3 (interfac\$4 protocol communication arbitration ) and simulat\$4

	Search Text
1	S10 and emulat\$5 same (signal (bidirection\$4 bi adj direction\$4 unidirection\$4) same ((one single uni) adj direction\$4) unidirection\$4 )
2	(support\$6 conver\$6 transform\$6) same (bidirection\$4 bi adj direction\$4) same (unidirection\$4 (one single uni) adj direction\$4)
3	S3 and emulat\$5 same tap\$7
4	simulat\$6 with signal and S3
5	S3 and (tri-state tristate) same (buffer driver)
6	S3 and emulat\$5 same (tri-state tristate) same (buffer driver)
7	S2 and emulat\$4 same (bidirection\$4 bi adj direction\$4) same (unidirection\$4 (one single uni) adj direction\$4)
8	S1 and simulat\$5
9	emulat\$4 with (flow link traffic bidirection\$4 bi adj direction\$4 unidirection\$4 (one single uni) adj direction\$4) and (bidirection\$4 bi adj direction\$4) same (unidirection\$4 (one single uni) adj direction\$4) same (simulat\$6 emulat\$6)
10	S9 and emulat\$5 same (signal (bidirection\$4 bi adj direction\$4 unidirection\$4) same ((one single uni) adj direction\$4) unidirection\$4 )
11	S8 and simulat\$5 same (signal (bidirection\$4 bi adj direction\$4 unidirection\$4) (one single uni) adj direction\$4)
12	simulat\$5 same (system signal channel traffc communication link ) and emulat\$4 same direction\$5 same (channel line signal bus link) and (bidirection\$4 bi adj direction\$4 ) same ((one single uni) adj direction\$4 unidirection\$4 )